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CONFIRMATION NO. 3283

SERIAL NUMBER 10/751,521	FILING OR 371(c) DATE 01/06/2004 RULE	CLASS 438	GROUP ART UNIT 2812	ATTORNEY DOCKET NO. 60188-656	
APPLICANTS Hiroshi Takenaka, Kyoto, JAPAN; ** CONTINUING DATA ***** This application is a DIV of 09/812,947 03/27/2001 PAT 6,725,440 ** FOREIGN APPLICATIONS ***** JAPAN 2000-085775 03/27/2000 IF REQUIRED, FOREIGN FILING LICENSE GRANTED ** 04/09/2004					
Foreign Priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no 35 USC 119 (a-d) conditions <input type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after met Allowance Verified and Acknowledged Examiner's Signature _____ Initials _____		STATE OR COUNTRY JAPAN	SHEETS DRAWING 6	TOTAL CLAIMS 6	INDEPENDENT CLAIMS 1
ADDRESS 20277					
TITLE METHOD OF EXPOSING A SEMICONDUCTOR INTEGRATED CIRCUIT INCLUDING DEVICE REGIONS AND GLOBAL ROUTING REGION					
FILING FEE RECEIVED 1070	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:		<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees (Filing) <input type="checkbox"/> 1.17 Fees (Processing Ext. of time) <input type="checkbox"/> 1.18 Fees (Issue) <input type="checkbox"/> Other _____ <input type="checkbox"/> Credit		